

















































![](_page_12_Figure_1.jpeg)

![](_page_13_Figure_0.jpeg)

![](_page_13_Figure_1.jpeg)

![](_page_14_Figure_0.jpeg)

![](_page_14_Figure_1.jpeg)

![](_page_15_Figure_0.jpeg)

![](_page_15_Figure_1.jpeg)

![](_page_16_Figure_0.jpeg)

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![](_page_17_Picture_0.jpeg)

![](_page_17_Figure_1.jpeg)

![](_page_18_Figure_0.jpeg)

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![](_page_19_Figure_0.jpeg)

![](_page_19_Figure_1.jpeg)

![](_page_20_Figure_0.jpeg)

![](_page_20_Figure_1.jpeg)

![](_page_21_Figure_0.jpeg)

![](_page_21_Figure_1.jpeg)

## Loop shaping procedure

1. 2. 3 4. 5. du fu	<ul> <li>stabilize the plant:</li> <li>add lead/lag with zero = bandwidth/3 and pole = bandwidth*3, adjust gain to get stability; or add a pure PD with break point at the bandwidth</li> <li>add low-pass filter:</li> <li>choose poles = bandwidth*6</li> <li>add notch if necessary, or apply any other kind of first or second order filter and shape the loop</li> <li>add integral action:</li> <li>choose zero = bandwidth/5</li> <li>increase bandwidth:</li> <li>increase gain and zero/poles of integral action, lead/lag and other filters</li> </ul>	
Control for Dumm	ies	45

In	plemen	tation issues	
1. s	ampling =	delay: linear phase lag	
for due	example: e to Zero-C	sampling at 4 kHz gives phase lag Drder-Hold of:	
	180°	@ 4 kHz	
	18°	@ 400 Hz	
	9°	@ 200 Hz	
2.1	Delay due	to calculations	
3. (	Quantizatio	on (sensors, digital representation)	
	-		

![](_page_23_Figure_0.jpeg)

![](_page_23_Figure_1.jpeg)

![](_page_24_Figure_0.jpeg)

![](_page_24_Figure_1.jpeg)

![](_page_25_Figure_0.jpeg)

![](_page_25_Figure_1.jpeg)

![](_page_26_Figure_0.jpeg)

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![](_page_27_Figure_0.jpeg)

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![](_page_28_Figure_0.jpeg)

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![](_page_29_Figure_0.jpeg)

![](_page_29_Figure_1.jpeg)

![](_page_30_Figure_0.jpeg)

![](_page_30_Figure_1.jpeg)

![](_page_31_Figure_0.jpeg)